**DAC gain & ADC level to dBm conversion (calibration table)**

The data below are obtained using the bitstream: top\_216-pfb2.bit. link:

<https://github.com/Ri-chard-Wu/thesis/tree/master/codes/axis_pfb_readout_v2-demo-216>

For complete table data, see this link:

<https://github.com/Ri-chard-Wu/thesis/tree/master/codes/dacGain_adcLevel_to_dbm_conversion>

ADC level to dBm conversion table

* wiring: sgs100a → ADC balun 10-1000 MHz → sr0.
* The results for sr0 ~ sr7 are all similar.

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| ADC level to dBm conversion table |
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DAC Gain to dBm conversion table

* Wiring: sg6 → DAC balun 10-1000 MHz → ADC balun 10-1000 MHz → sr4.
* The results for all sg’s in the bitstream are all similar.
* Verified with oscilloscope. Deviate by less than 2dBm.

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| DAC Gain to dBm conversion table (const) |
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| DAC Gain to dBm conversion table (flat\_top) |
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